

SRI CHAKRAPANI YELLAMRAJU

Department: Electronics and Communication Engineering
College: Seshadri Rao Gudlavalleru Engineering College
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RESIDENTIAL ADDRESS:

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Gudivada – 521301.

ACADEMIC QUALIFICATIONS:

PhD (Pursing) in VLSI & Embedded systems.

Master of Technology in VLSI & Embedded systems.

Bachelor of Engineering in Electronics and Communication Engineering

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Associate Professor	Seshadri Rao Gudlavalleru Engineering College	30-10-2012 to till date
Assistant Professor	Bapatla Engineering College	07-11-2007 to 29-10-2012
Project Intern	VSBU, Wipro Technologies, Bangalore	01-01-2005 to 30-04-2005

PROJECTS GUIDED:

Post Graduation	Under Graduation
11	31

SPECIALIZATION: VLSI & Embedded systems

PATENTS PUBLISHED: 2

1. **Y.Sri Chakrapani**, N.Venkateswara Rao, M. Kamaraju, “Efficient VLSI Architecture for Sobel Edge Detection with Kogge-Stone Adder”, The Patent Office Journal No. 21/2023, published date 19/04/2024, page no. 37409, Application Number 202441030159A, Date of filling 15/04/2024.
2. Dr. M. Vaigundamoorthi, Dr. V Kamalakar, Narender Chinthamu, Mr. Jayanta Kumar Sahu, R.Kavin M.E , **Mr. Y.Sri Chakrapani**, Dr. Ch Manohar Kumar, Dr. Lalchand Patle, Mrs. Shaik Salma Begum, Manideep Karukuri, “IOT-based photovoltaic solar power generation with maximum power point TRACKING”, The Patent Office Journal No. 16/2024, published date 26/05/2023, page no. 39161, Application Number 202341026559A, Date of filling 10/04/2023.

PAPERS PUBLISHED IN JOURNALS: 19

1. **Y. Sri Chakrapani**, P. Prathyusha, N. Leela Pratap, N. Mounika, S. Naga Rohith, “Advanced Car Parking System with Real Time Availability and Booking”, Journal of Emerging Technologies and Innovative Research, ISSN: 2349-5162, April 2024. <https://www.jetir.org/papers/JETIR2404847.pdf>.
2. **Y. Sri Chakrapani**, M. Snehitha, G. Kedar, K. Amarnadh, H. Lokesh, “Renewable Power Generation for Smart Security System”, Journal of Emerging Technologies and Innovative Research, ISSN: 2349-5162, April 2024. <https://www.jetir.org/papers/JETIR2404027.pdf>
3. **Y. Sri Chakrapani**, A. Rohita Sai, B. Jo Sri Ganesh, B. Pawan Kalyan, E. Swathi, “FPGA-Based Low-Power Sobel Edge Detection System”, International Research Journal of Modernization in Engineering Technology and Science, e-ISSN: 2582-5208, Volume 05, Issue 04, April 2023.
4. **Y. Sri Chakrapani**, K. Monalika, M. Dhanush, K. Nithish, L. Anil Kumar, “Development of Firmware for Air Pollution Detection System”, International Research Journal of Modernization in Engineering Technology and Science, e-ISSN: 2582-5208, Volume 05, Issue 04, April 2023.
5. **Y. Sri Chakrapani**, CH.V.D Ashok Kumar, “Implementation and Analysis of 32-Bit TCAM (Ternary Content Addressable Memory) using FPGA”, Journal of Emerging Technologies and Innovative Research, September 2022, Volume 9, Issue 9, pp:659-663.
6. **Y. Sri Chakrapani**, CH.V.D Ashok Kumar, “A Survey Paper of TCAM Architectures”, Journal of Emerging Technologies and Innovative Research, September 2022, Volume 9, Issue 4, pp:376-383.
7. **Y Sri Chakrapani**, N Venkateswara Rao, M Kamaraju, “Implementation of Low Power Memory Architecture for Sobel Edge Detection”, Design Engineering (Toronto), Volume 2021, Issue 9, pp: 14892-14899. <https://www.thedesignengineering.com/index.php/DE/article/view/8695>

8. **Y Sri Chakrapani**, N Venkatewsara Rao, M Kamaraju, "A Survey of Sobel Edge Detection VLSI Architectures", Journal of Physics: Conference Series, Volume 1804, Issue (2021) 012151 DOI:10.1088/1742-6596/1804/1/012151, [Scopus Indexed] Print ISSN: 17426588, E-ISSN: 17426596. <https://iopscience.iop.org/article/10.1088/1742-6596/1804/1/012151>.
9. Surapaneni Aparna, T. Venkata Lakshmi, M. Kamaraju, **Y.Sri Chakrapani**, "Implementation of Deep Learning Accelerator Unit", International Journal of Engineering Research & Technology (IJERT), vol. 8, issue 07, July-2019, ISSN: 2278-0181.
10. Surapaneni Aparna, **Y. Sri Chakrapani**, T. Venkata Lakshmi, M. Kamaraju, "A Survey of VLSI Architectures for Hamming Code Algorithm", International Journal of Advanced Scientific Research and Management, Special issue February 2019, pp. 1-10, CITAES-18, ISSN 2455-6378.
11. Y. Mamillu, **Y. Sri Chakrapani**, M. Kamaraju, "FPGA Implementation of Integral Image generator in SURF detector", International Journal of Electronics Engineering, Volume 10, Issue 2, pp. 233-240, June 2018.
12. J.Naga Surekha, **Y. Sri Chakrapani**, M. Kamaraju, "Implementation of High Performance Speeded Up Robust features Detection", International Journal of Scientific & Engineering Research Volume 8, Issue 5, May 2017.
13. J.Naga Surekha, **Y. Sri Chakrapani**, N.Venkatewsara Rao, "A Survey of Trends in Local Invariant Feature Detectors", International Journal of Electrical and Electronic Engineering & Telecommunications, Volume 2, Issue 5, April 2017.
14. K. Sai Kumar, **Y.Sri Chakrapani**, M. Kamaraju, "Implementation of Low Power Ultrasonic Phased Array Using ZYNQ SoC", International Journal of Computer Science and Information Technology & Security, Volume 6, Issue 4, July August 2016.
15. Y. Sasi Rekha, **Y.Sri Chakrapani**, M. Kamaraju, "Design and Implementation of Low Power, Light Weight Cryptographic Core using Hummingbird2 Algorithm", International Journal of Scientific & Engineering Research, Volume 4, Issue 35, August-2015.
16. K.Sindhu, **Y.Sri Chakrapani**, M. Kamaraju, "FPGA Implementation of Irrigation Control System", International Journal of Scientific & Engineering Research, Volume 5, Issue 12, December-2014.
17. M.Aparna, **Y.Sri Chakrapani**, M. Kamaraju, "FPGA Implementation of an Agricultural Management System using Wireless Networks", International Journal of Computer Applications, Volume 105 – No. 18, November 2014.
18. S.Venkata Kishore, **Y.Sri Chakrapani**, M. Kamaraju, "Design and Implementation of High Speed Data Transmission over Dual Independent Aurora Channels on One GTX dual tile using Virtex-5 FPGA", International Journal of Scientific & Engineering Research, Volume 4, Issue 12, December-2013.
19. V. Krishna Kumari, **Y.Sri Chakrapani**, M. Kamaraju, "Design and Characterization of Koggestone, Sparse Koggestone, Spanning tree and Brentkung Adders", International Journal of Scientific & Engineering Research, Volume 4, Issue 10, October-2013.

PAPERS PUBLISHED IN CONFERENCES: 09

1. **Y. Sri Chakrapani**, K. Chandrakala Kanumuri, K. Vijayalakshmi, J. Hynar, K. Manoj Kumar Kasarapu, “Implementation of High-Speed VLSI Architecture for Edge Detection” at Second International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES’22), organized by the Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, India during July 29 - 30, 2022.
2. **Y. Sri Chakrapani**, S. Prasanthi, P. Harichandana, S. Manoharsha, T. Maruthi Rao, “Implementation of High-Speed VLSI Architecture for Edge Detection” at Second International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES’22), organized by the Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, India during July 29 - 30, 2022.
3. Y.Mamillu, **Y. Sri Chakrapani**, N.Venkateswara Rao, “FPGA Implementation of Integral Image generator in SURF detector”, Online National conference on Communications and Computing (NC3-2020), organized by Department of Electronics and Communication Engineering, Bapatla Engineering College on 28th & 29th July 2020.
4. G.Poojitha, K.Uday Bhaskar, K.Vinay, P.Vijay kumar, **Y. Sri Chakrapani**, “Implementation of Sobel Edge Detection using FPGA”, Online National conference on Communications and Computing (NC3-2020), organized by Department of Electronics and Communication Engineering, Bapatla Engineering College on 28th & 29th July 2020.
5. Surapaneni Aparna, **Y. Sri Chakrapani**, T. Venkata Lakshmi, M. Kamaraju, “A Survey of VLSI Architectures for Hamming Code Algorithm”, CITAES-18, December 2018, Gudlavalleru Engineering College, Gudlavalleru.
6. J.Naga Surekha, **Y. Sri Chakrapani**, N.Venkateswara Rao, “A Survey of Trends in Local Invariant Feature Detectors”, National Conference on Emerging Trends in Electronics and Communications Engineering, Department of Electronics and Communications Engineering, Bapatla Engineering College on 20th & 21st January 2017.
7. **Y.Sri Chakrapani**, M. Kamaraju, N.Venkateswara Rao, “FPGA Implementation of low power UART”, National Conference on RF Wireless Communication & Signal Processing (NCRFWCSP) 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
8. M.Aparna, **Y.Sri Chakrapani**, M. Kamaraju, “ FPGA Implementation of an Agricultural Management System using Wireless Networks”, National Conference On RF Wireless Communication & Signal Processing (NCRFWCSP) 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
9. **Y.Sri Chakrapani**, A.V.Sri Harsha, G. Nitin, “Implementation of Elliptic Curve Digital Signature Algorithm For Security Applications”, National Conference on Social Networking, Aurora’s Engineering College, Bhongir on 16th & 17th March 2012.

LIST OF WORKSHOPS ORGANIZED: 15

1. Two Day Training Programme on “Design of Integrated Circuits using Microwind Tools”, 30th - 31th August 2023 at Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru.
2. Two Day Faculty Development Programme on “Network Simulation with NetSim Software”, 13th - 14th October 2022 at Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru.
3. Two Day Faculty Training Programme on “Design and development of VLSI circuits using Mentor Graphics Tools”, 14th - 15th December 2020 at Gudlavalleru Engineering College, Gudlavalleru.
4. Two Day Faculty Development Programme on “Design and development of digital circuits using VLSI Tools”, 6th - 7th December 2018 at Gudlavalleru Engineering College, Gudlavalleru.
5. Two Day Faculty Training Programme on “Advances in VLSI Design and Embedded systems”, 6th - 7th November 2017 at Gudlavalleru Engineering College, Gudlavalleru.
6. Two Day Training Programme on “Mentor Graphics Tools”, 31st August – 1st September, 2017 at Gudlavalleru Engineering College, Gudlavalleru.
7. One day seminar on “Memory Design Techniques”, 08th January 2016 at Gudlavalleru Engineering College, Gudlavalleru.
8. One day seminar on “Advanced VLSI Design: special topics”, 08th June 2015 at Gudlavalleru Engineering College, Gudlavalleru.
9. Two day workshop on “Embedded System Design and Programming”, 19th to 20th December 2014 at Gudlavalleru Engineering College, Gudlavalleru.
10. Workshop on “Low Power VLSI Design”, 9th August 2014 at Gudlavalleru Engineering College, Gudlavalleru.
11. Refresher Course on “Digital System Design & Digital IC Applications”, 16th to 20th June 2014 at Gudlavalleru Engineering College, Gudlavalleru.
12. Five Day Training Program on “Design Of Integrated Circuits Using Mentor Graphics Tools”, 21st to 25th January 2014 at Gudlavalleru Engineering College, Gudlavalleru.
13. Three Day Workshop on “VLSI Design”, 8th to 10th February 2013 at Gudlavalleru Engineering College, Gudlavalleru.
14. Two Day Workshop on “ROBOTICS”, 12th to 13th March 2011 at Bapatla Engineering College, Bapatla.
15. Two Day Workshop on “VLSI Design Flow”, 2nd to 3rd March 2008 at Bapatla Engineering College, Bapatla

LIST OF WORKSHOPS /FDPS /SEMINARS ATTENDED: 77

1. AICTE Training And Learning (ATAL) Academy Faculty Development Program on “Power-Efficient VLSI-Based Hardware Accelerators in High-Performance Computing (HPC)” organized by Birla Institute of Technology and Science, Pilani from 20th to 25th January 2025.

2. One-Week Faculty Development Program (FDP) on "Nanotechnology/Quantum Technology / Semiconductor Device Simulation & Experimental Characterization" organized by Nano-academic Technologies during 25th to 29th November 2024.
3. 40 hours Faculty Development Programme on "Advancement in Communication Systems: Integrating AI & ML for Enhanced Signal Processing" sponsored by Ministry of Electronics and Information Technology (MeitY), GoI organized by E&ICT Academy, NIT Warangal, Telangana State and G. Narayanamma Institute of Technology & Science (For Women) Hyderabad during 11th to 21st November, 2024.
4. Online Faculty Development Programme (FDP) on "IOT and Embedded Systems" by (NITTTR), Chandigarh and PVPSIT, Vijayawada during 23-09-2024 to 27-09-2024.
5. Eight days Face-to-Face UHV-II FDP organized by All India Council for Technical Education (AICTE) at Sri Vasavi Engineering College, West Godavari during 17th to 24th July 2024.
6. One Week Online FDP on "Impact of AI: Robotics, VLSI and Medical Domains" organized by the Department of ECE in association with IETE Vijayawada Centre, during 22nd to 27th April 2024.
7. One Week online Faculty Development Programme on "Emerging Trends and Challenges in Communication and Computational Technologies (5G/6G)" Organized by Department of Electronics and Communication Engineering in Association with IEEE Communication Society and MTT-Society of VNRVJIET, Hyderabad during 18th to 22th March 2024.
8. Online National Webinar on "Low Power VLSI Design Challenges: A View" organized by VLSI & Microelectronics Research Group, Department of ECE, K L University during 16th March 2024.
9. Five days Online Faculty Development Program, "Recent Advancement in Sustainable Energy Sources: Research and Development", organized by Department of Physics, Karpagam Academy of Higher Education, Coimbatore, during 26th February to 1st March 2024.
10. One Week Refresher Course on "Transmission Lines and Waveguides" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalluru Engineering College during 29th January to 2nd February 2024.
11. One Week Refresher Course on "Microcontrollers and Embedded Systems" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalluru Engineering College during 9th to 13th January 2024.
12. One Week online Faculty Development Program on "Quantum Signal Processing", organized by Department of Electronics & Communication Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram, NTR District, In Association with IETE, Vijayawada center held during 14th to 20th December 2023.
13. Two Day Training Programme on "Design of Integrated Circuits using Microwind Tools", organized by department of ECE, Seshadri Rao Gudlavalluru Engineering College, Gudlavalluru during 30th to 31th August 2023.
14. One Week National Level Faculty Development Program on Recent Advances in Electronics and Communication Engineering – An approach through AI & ML" conducted

- by the department of ECE, Vignan's Nirula Institute of Technology and Science For Women during 21st to 26th August 2023.
15. One Week Faculty Development Program on "Applications of Computer Vision using Deep Learning" conducted by the department of ECE, Seshadri Rao Gudlavalleru Engineering College during 20th to 25th February 2023.
 16. Two Day Faculty Development Programme on "Network Simulation with NetSim Software", organized by department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru 13th to 14th October 2022
 17. Blended mode Professional Development Program on "Outcome Based Education and Design Thinking" Conducted by IEEE Guntur Subsection and Department of Electrical and Electronics Engineering, KLEF Deemed to be University during 7th to 15th October, 2022.
 18. Faculty Development Program on "Applied Robot Control Faculty Development Program", organized by FESTO India Pvt. Ltd., Bengaluru in collaboration with Indo - Euro Synchronization, Vijayawada, during 24th to 26^h August 2022.
 19. A Two-day workshop on "Machine Learning: Concepts and Applications" organized by R&D Cell, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, during 30th June to 01st July, 2022.
 20. One day Faculty Development Program on "Intellectual Property Rights", organized by IQAC and ISTE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, during 26th March 2022.
 21. One Week Online Faculty Development Program on "Trends and Challenges in Advanced VLSI System Design" organized by Dept. of E.C.E, S.R.K.R Engineering College(A), from 21st to 26th February 2022.
 22. A 40-hour online FDP on "Introduction to low power VLSI Design and applications", organized by Dept of ECE and E&ICT Academy, NIT Warangal from 23rd December 2021 to 3rd January 2022.
 23. Five Series Webinar on "Introduction to Radar Systems" organised by NITK, Surathkal Jointly with Sri Shasha Prayathi Technologies Pvt. Ltd. from 4th September to 4th October 2020
 24. A National Level Online One Week Faculty Development Program on "Recent Advances in VLSI & Embedded Systems (RAVDES -2020)", Organized by Micro Electronics Research Group (MERG), Department of Electronics and Communication Engineering, Koneru Lakshmaiah Education Foundation (KL Deemed to be University), Vaddeswaram, Guntur, Andhra Pradesh, India in Association with IETE Vijayawada Chapter from 14th to 19th September 2020.
 25. An AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" Phase-I, II, III organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 10th to 29th August 2020.
 26. A National Level Webinar on "Intellectual Property Rights & Patents - A View" organized by IQAC, Gudlavalleru Engineering College, Gudlavalleru from 10th to 12th August 2020.

27. A One week STTP on “Recent Trends In Wireless Sensor Networks & Applications”, organized by Malla Reddy Engineering College for Women, from 3rd to 8th August, 2020.
28. A Refresher Course on “Embedded System Design”, organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru, from 27th July to 1st August 2020.
29. A National Level Faculty Development Program on “Cyber Security” organized by Department of CSE, Gudlavalleru Engineering College, from 22nd to 18th July 2020.
30. A One Week FDP on “Recent Advances in Biomedical Applications and Communications Networks”, organized by Department of ECE, GMRIT, Rajam, from 13th to 18th July 2020.
31. A One Week FDP on “Exposure & Rejuvenation of Technologies in changed Era of the World”, organized by Department of ECE, Raj Kumar Goel Institute of Technology, Ghaziabad, from 6th to 10th July 2020.
32. A National Level Faculty Development Program on “PCB Design” organized by Department of ECE, VVIET, Mysuru in association with Sienna ECAD Technologies and ESSCI from 29th June 2020 to 03rd July 2020.
33. A One Week FDP on "Role of Machine Learning & Data Sciences in AI", organized by Department of CSE and M&H, Mahatma Gandhi Institute of Technology, Hyderabad, from 28th June to 2nd July 2020.
34. A National Level Five Day Online Faculty Development Programme on “Scilab and NgSpice” organized by the Department of Electronics & Communication Engineering in association with Spoken Tutorial, developed by IIT Bombay at Universal College of Engineering and Technology, Guntur, from 22nd to 26th June 2020.
35. A Five day online FDP on “Opportunities and Challenges in Next-Generation Semiconductor Devices" organized by Department of Electronics and communication Engineering, Anil Neerukonda Institute of Technology and Sciences, Visakhapatnam from 16th to 20th June, 2020.
36. A One Week International Online Knowledge Development Program on "Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: Researchers View" organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, from 8th to 13th June , 2020.
37. A webinar on "Impact of COVID in Engineering Education & Way forward" organized by Indian Society for Technical Education (ISTE) Chapter, Gudlavalleru Engineering College, Gudlavalleru on June 9th 2020.
38. A National Level Online FDP on "Recent Trends in Communication and Wireless Networks" organized by KKR & KSR Institute of Technology & Sciences, Guntur, from June 5th to 9th, 2020.
39. A One Week Online E-certification FDP on “Industrial IOT using LoRa Technology” organized by Vemu Institute of Technology, College and Apply volt from June 1st to 5th, 2020.
40. A One Week National Level Online Faculty Development Program on "Research Challenges and Emerging Trends in Electronics and Communication Engineering

(RCEECE2020) " organized by Ramachandra College of Engineering, Eluru from May 27th to June 1st , 2020.

41. Five Day National Level Online FDP on “Modern Research Trends in Communication, Signal Processing and VLSI/MEMS”, organized by the Department of Electronics and Communication Engineering, Andhra Loyola Institute of Engineering and Technology, Vijayawada, during 19th to 23rd May 2020.
42. One week FDP on “Data Acquisition and RT using LabVIEW” organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 25th – 30th November 2019.
43. A Three day FDP on “Advanced Development Tools Based IoT” organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 11th – 13th November 2019
44. A Two day workshop on "Introduction to Robotics”, conducted by IIT, Bombay at Saint Pious X Degree & PG college For Women, Hyderabad on 23rd – 24th July, 2019
45. A Short Term Training Programme through ICT mode on “Evaluating Student’s Performance and Designing Questions Papers”, conducted by NITTR, Kolkata at Gudlavalleru Engineering College, Gudlavalleru on 25th February 2019 to 1st March 2019,
46. A Two Day Training Programme on “Design and Development of Digital Circuits using VLSI Tools”, at Gudlavalleru Engineering College, Gudlavalleru on 6th – 7th December 2018..
47. A One Week FDP on “Trends in Reconfigurable (FPGA) SoC Design” at NIT Warangal on 25th – 30th November 2018.
48. A one week Faculty Development Program on “Communications and Signal processing”, at Gudlavalleru Engineering College, Gudlavalleru from 29th October to 3rd November 2018.
49. A Two day workshop on "Custom IC Design using Mentor EDA Tools” at Bapatla Engineering College, Bapatla on 16th- 17th April, 2018.
50. A one week National level Faculty Development Program on “Signal and Image processing using LabView”, at Gudlavalleru Engineering College, Gudlavalleru on 13th – 18th November 2017.
51. A Two Day Faculty Training Programme on “Advances in VLSI Design and Embedded systems”, at Gudlavalleru Engineering College, Gudlavalleru on 6th - 7th November 2017.
52. A Two Day Training Programme on “Mentor Graphics Tools”, at Gudlavalleru Engineering College, Gudlavalleru 31st August – 1st September, 2017.
53. A Two day National Workshop on “Advances in Electronics and Communication Engineering”, at JNTUK, Kakinada on 26th – 27th August, 2017.
54. A Two week STTP on “CMOS Mixed Signal and Radio Frequency VLSI Design” at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 30th January– 4th February 2017.
55. A one week National level Faculty Development Program on “Hands on experience in Signal Processing Applications”, at Gudlavalleru Engineering College, Gudlavalleru on 7th – 12th November 2016.

56. A Two Day Training program on "Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim at Gudlavalleru Engineering College, Gudlavalleru on 4th -5th November 2016.
57. A Two Day National Convention of Electronics and Telecommunication Engineers and National Seminar on "Intelligent System for more Connected World: Internet of Things" at Vijayawada on 24th - 25th October 2016.
58. A one day workshop on "Brainstorming on innovation in incubation centres" at JNTUK, Kakinada on 1st September 2016.
59. A Five Day Refresher course on "Microprocessors and Microcontrollers" at Gudlavalleru Engineering college, Gudlavalleru on 17th - 21st November 2015.
60. A Two Day All India Workshop on Emerging Techniques on "VLSI Architecture Design for Real Time Environment" at PACE Institute of Technology & Sciences, Ongole on 9th - 10th October 2015.
61. A Five Day Workshop on "Low power System on Chip Design using Xilinx FPGA Devices" at NIT Warangal on 3rd - 7th October 2015.
62. All India Seminar on "Research in Energy management, wireless networks & Smart Transportation systems" at Institute of Engineers, Vijayawada Local Centre on 29th March 2015.
63. A Two Day All India Seminar on "Agricultural And Horticultural Applications Of Unmanned Vehicles For Micro – Farming" at Shri Vishnu Engineering College for Women on 26th to 27th March 2015.
64. A Workshop on "Ultra Low Power Communication using CC430 Microcontroller" at Gudlavalleru Engineering College, Gudlavalleru on 7th February 2015.
65. A One day seminar on "Research in Signal and Image Processing on FPGA's and Embedded platforms" at Institute of Engineers, Vijayawada Local Centre on 22nd November 2014.
66. A Two day National Conference on RF Wireless Communication & Signal Processing (NCRFWCSP) November 17th – 18th 2014, Department of Electronics and Communications Engineering, Bapatla Engineering College.
67. A One day workshop on "Speech Processing: Current Challenges and Hands on experience" at Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
68. A Two week ISTE Workshop on "Signals & Systems" at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 2nd – 12th January 2014.
69. A Two Day Workshop on "Issues in Integrated Circuits (IC) Design" at Gudlavalleru Engineering College, Gudlavalleru on 13th – 14th December 2013.
70. A Two week ISTE Workshop on "Analog Electronics" at Gudlavalleru Engineering College, Gudlavalleru, conducted by IIT Kharagpur on 4th – 14th June 2013.
71. A Training program on "MATLAB, Simulink & Related Tool Boxes for Engineering Education" at Gudlavalleru Engineering College, Gudlavalleru, conducted by Academy of Design & Architecture on 3rd – 4th April 2013.
72. Participated in "Advanced Embedded Systems" workshop at CDAC, Hyderabad from 18th to 29th June 2012.

73. A Two day National Conference on Social Networking, Aurora's Engineering College, Bhongir on 16th & 17th March 2012.
74. Participated in "VLSI Design & Testing" workshop at PBR VITS, Kavali on 2nd and 3rd December 2011.
75. Participated in "FPGA Based DSP Implementation" using XILINX Workshop at GMRIT, Rajam on 26th and 27th November 2010.
76. Participated in "VLSI Design Software" Workshop at JNTU, Kakinada on 27th and 28th September 2008.
77. Participated in "Low Power VLSI Design" short term course at SNIST, Hyderabad from 3rd to 8th December 2007.

CERTIFICATIONS:

1. NPTEL Course "CMOS Digital VLSI Design" with 81% (Elite + Silver).
2. Audit Course "Research Methodologies" during May 1st – 15th, 2015 at JNTUK, Kakinada.
3. Audit Course "Intellectual Property Rights" during May 16th – 30th, 2015 at JNTUK, Kakinada.
4. Completed Course "Nanoelectronics & Its Industrial Applications" at Nano Science and Technology Consortium, Noida.

GUEST LECTURES DELIVERED:

1. One Week Orientation programme on "Computer Networks" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 18th to 22nd June 2024.
2. One Week Refresher Course on "Microcontrollers and Embedded Systems" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 9th to 13th January 2024.
3. A Refresher Course on "Embedded System Design :: State Machine and Concurrent Process Models", 30th July 2020 at Gudlavalleru Engineering College, Gudlavalleru.
4. Faculty Seminar on "Carbon Nanotubes", 25th September 2014 at Gudlavalleru Engineering College, Gudlavalleru.
5. A Refresher Course on "Digital System Design & Digital IC Applications", 19th June 2014 at Gudlavalleru Engineering College, Gudlavalleru.
6. A Refresher Course on "Electronic Devices and Circuits", 24th September 2013 at Gudlavalleru Engineering College, Gudlavalleru.
7. Faculty Seminar on "System on Chip", 20th February 2013 at Gudlavalleru Engineering College, Gudlavalleru.

MEMBERSHIP IN PROFESSIONAL BODIES:

S. No	Name of the Professional body	Membership Number
1	IEI	M-151044-9

2	IETE	M-503578
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CONSULTANCY ACTIVITIES:

Sl. No.	Name of the Industry	Details of consultancy undertaken	Cost of consultancy	Details of Faculty involved
1	SAK informatics, Vijayawada	Automatic Speech Emotion Recognition using Self-attention based Deep Learning with Optimal Feature Selection Approach	Rs. 50,000	Dr. M. Kamaraju, Dr. B. Rajasekhar, Mr. Y.Sri Chakrapani , Mr. G. Ramakrishna

INTERNAL R&D PROJECTS:

S.No.	Title of the Project	Name of Investigators (s)	Funding Agency	Cost	Status
1	Development of virtual eye system for visually impaired using Field Programmable Gate Array	Mr. Y. Sri Chakrapani , Dr.T.Subhashini	SRGEC	Rs 90,000/-	On-going
2	Assistive Technologies for Virtually impaired Proposal	Mr. Y.Sri Chakrapani	SRGEC	Rs. 2,500	Completed

ACADEMIC REWARDS / ACHIEVEMENTS

- Selected in written test and Interview conducted for PhD at JNTUK, Kakinada.
- Best Faculty award for the academic year 2009-10 at Bapatla Engineering College, Bapatla.
- Best Participant award for Low Power VLSI Workshop at SNIST, Hyderabad conducted from 3rd to 8th Dec 2007.
- Selected in Written Test and Interview conducted for M.Tech VLSI at IIIT, Hyderabad.
- First 2% in EAMCET 1999.

DECLARATION

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date:

Signature

Place: Gudlavalleru

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